**Application No.: 10/559,867** 

## **REMARKS**

## I. <u>Introduction</u>

In response to the Office Action dated May 29, 2007, Applicants have amended claims 12 and 14 to more particularly point out and distinctly claim the subject matter of the invention. Care has been taken to avoid the introduction of new matter. In view of the foregoing amendments and the following remarks, Applicants submit that all pending claims are in condition for allowance.

As a preliminary matter, it is noted that on the "Office Action Summary", only claims 12 – 15 are listed as pending in the application. Additionally, the Examiner has only addressed claims 12 – 15 in the Detailed Action. However, the pending application includes claims 12 – 17. Applicants respectfully request consideration of all pending claims in the next Office Action.

## II. Claim Objections

The Examiner has objected to claims 12 and 14 as being awkwardly written. Applicants have amended these claims to more particularly describe the subject matter of the invention.

Accordingly, withdrawal of the objections to the claims is requested.

## III. Claim Rejections Under 35 U.S.C. § 102

Claims 12 – 15 stand rejected under 35 U.S.C. § 102(b) as allegedly being anticipated by U.S. Patent No. 5,933,037 to Momtaz. Applicants traverse these rejections for at least the following reasons.

Claim 12 recites, among other things, a PLL frequency synthesizer comprising a linearization circuit for controlling a gain of the charge pump circuit so as to compensate for nonlinearity of a loop gain of the PLL frequency synthesizer with respect to the oscillator frequency control signal, wherein the linearization circuit has a plurality of transistors for receiving the oscillation frequency control signal from the loop filter and changing flowing